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<b>To:</b>	Examiner James R. Harvey	<b>From:</b>	Timothy N. Trop
<b>Company:</b>	US Patent Office	<b>Date:</b>	August 21, 2006
<b>Fax:</b>	(571) 273-8300	<b>Pages:</b>	7 (including cover sheet)
<b>Your Re:</b>	10/822,572	<b>Our Re:</b>	ITL 1113US

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AUG 21 2006

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	
Shawn L. Lloyd et al.	§	Art Unit: 2833
	§	
Serial No.: 10/822,572	§	Examiner: James R. Harvey
	§	
Filed: April 12, 2004	§	Docket: ITL.1113US
	§	P18785
For: Making Electrical Connections	§	
Between a Circuit Board and	§	Assignee: Intel Corporation
an Integrated Circuit	§	

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO FINAL REJECTION**

Sir:

In response to the final rejection mailed August 7, 2006, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: August 21, 2006

I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No., 571/273-8300) on the date indicated above.

*Cynthia L. Hayden*  
Cynthia L. Hayden